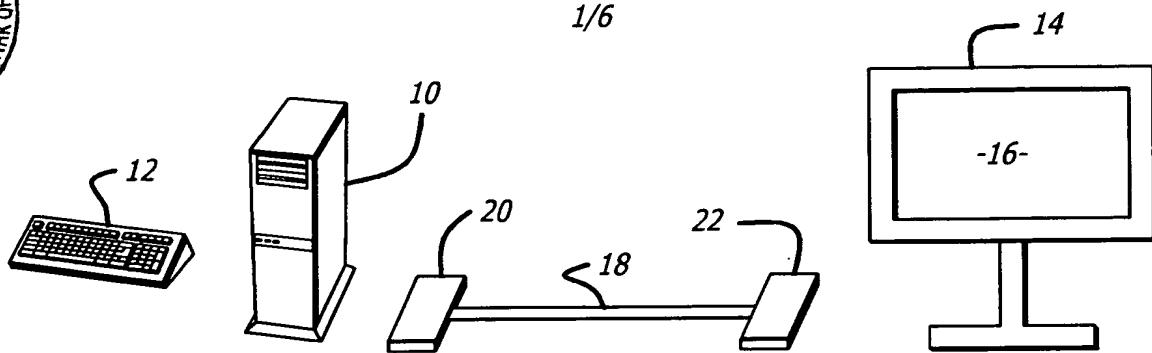
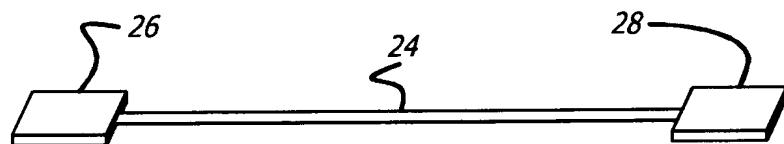


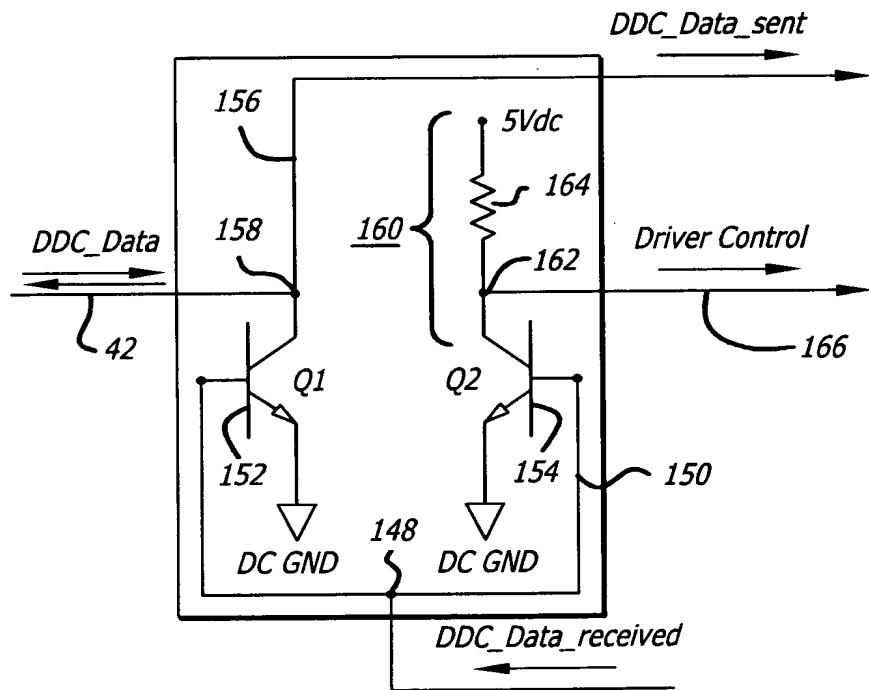
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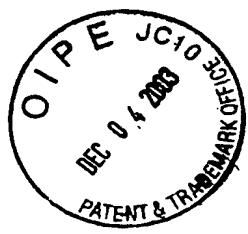
*FIG. 1A*  
(Prior Art)



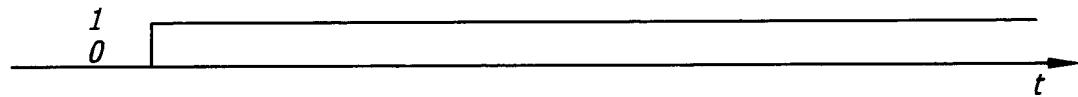
*FIG. 1B*



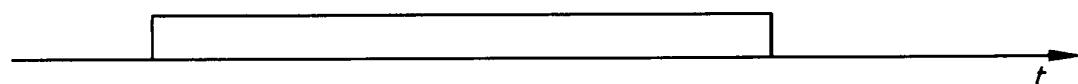
*FIG. 6*



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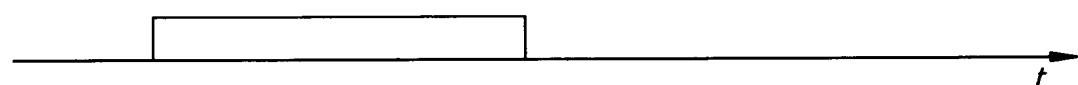


*FIG. 3A*



*B:DDC\_DATA\_SENT*

*FIG. 3B*



*A:DDC\_CLOCK*

*FIG. 3C*

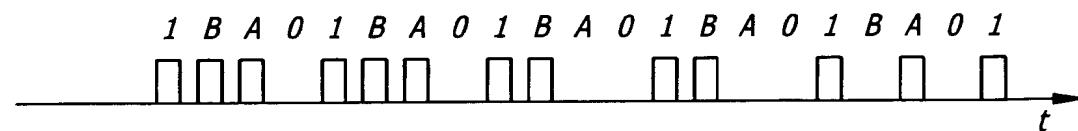


*FIG. 3D*

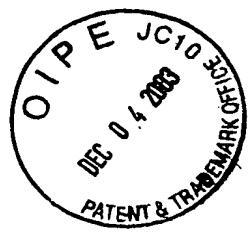


*MUX CLOCK*

*FIG. 3E*



*FIG. 3F*



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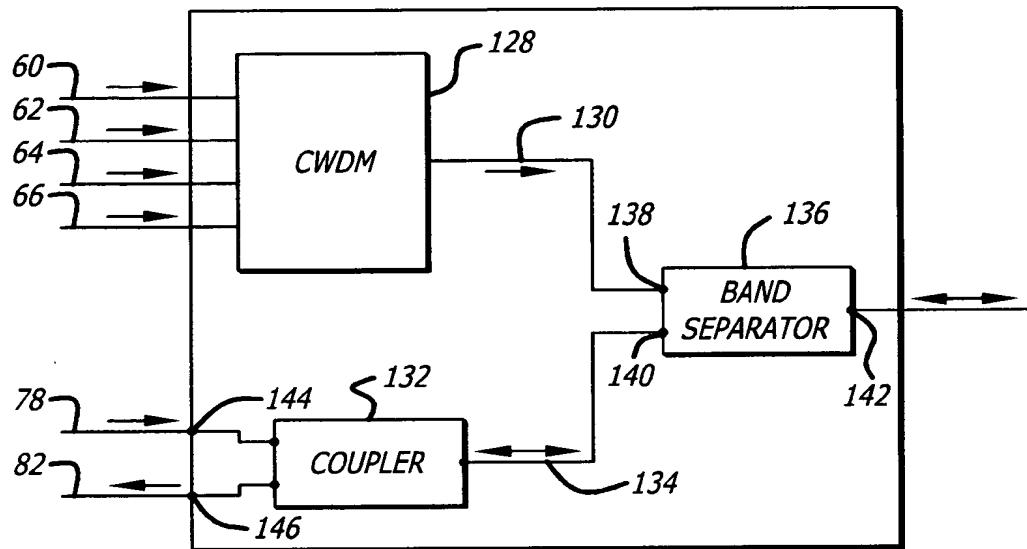


FIG. 5A

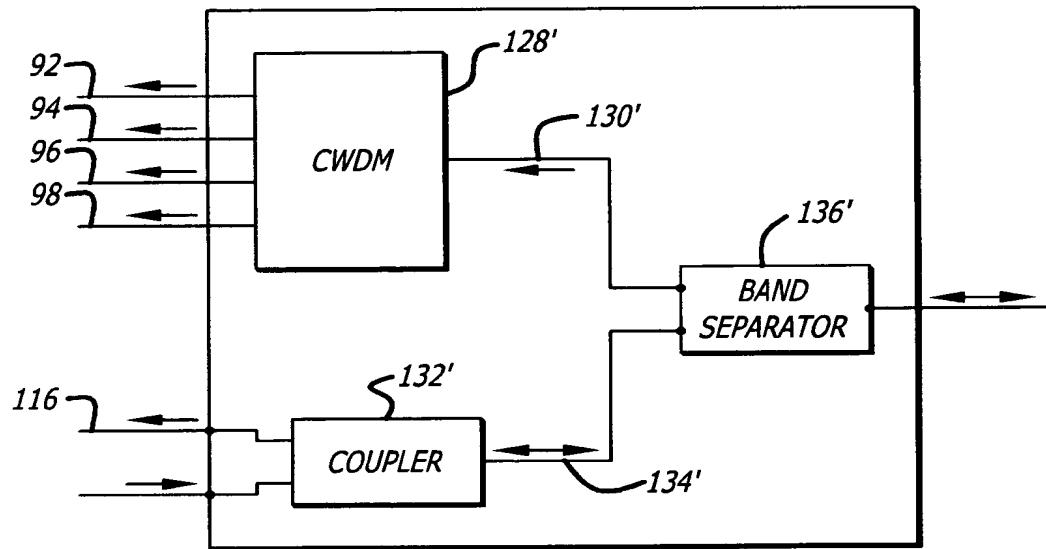


FIG. 5B



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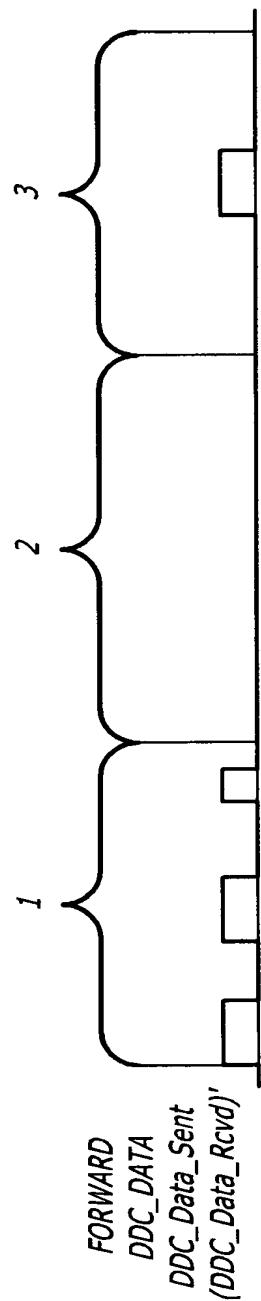


FIG. 7A

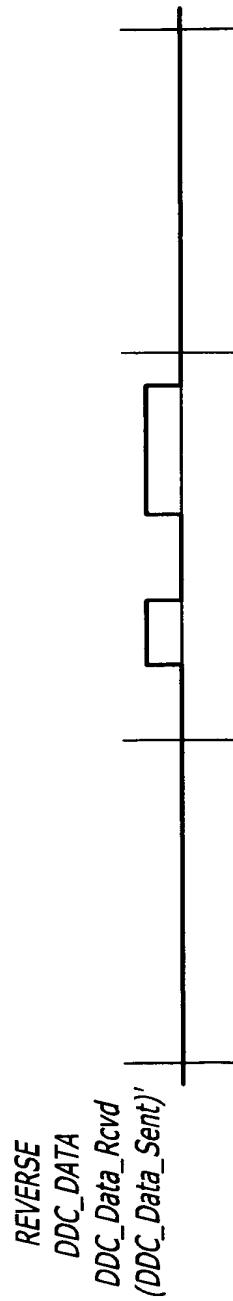


FIG. 7B